

FIG. 1

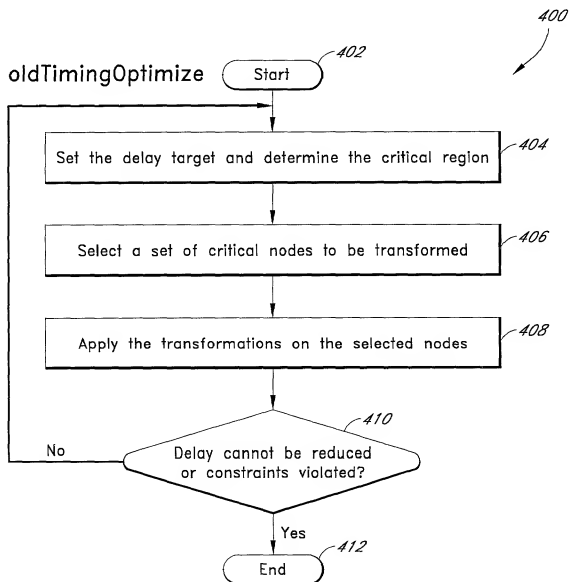


FIG. 2

Local Transformation

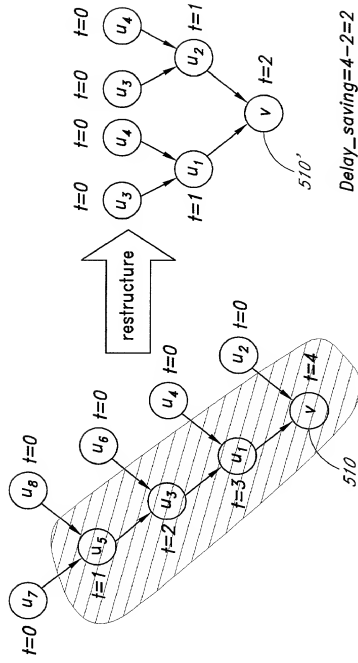


FIG. 3

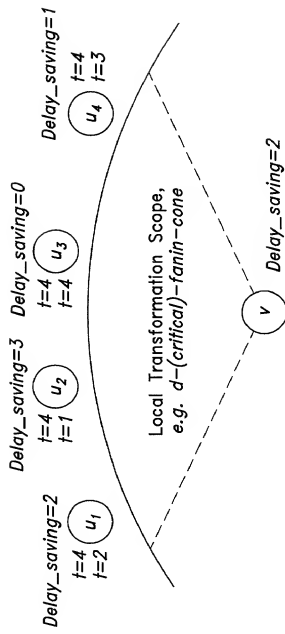


FIG. 4

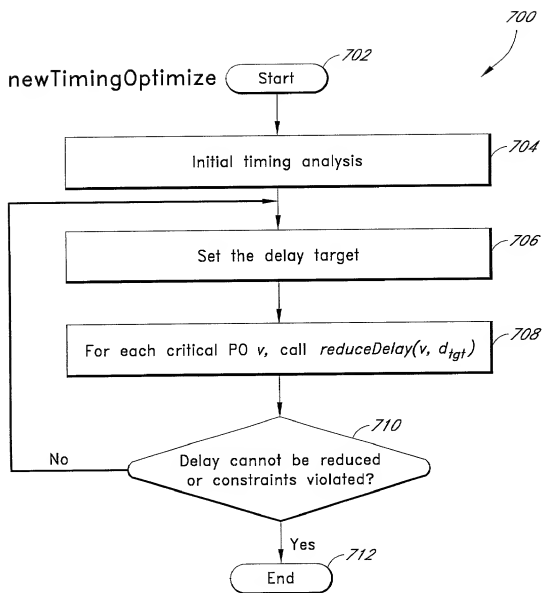


FIG. 5

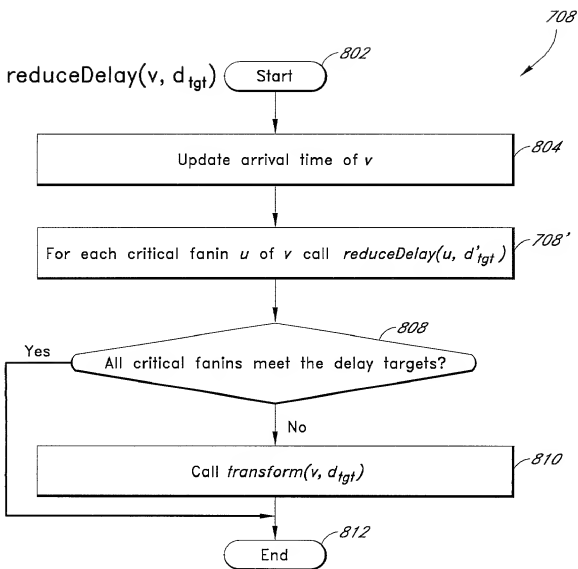


FIG. 6

Example of Recursive Delay Reduction

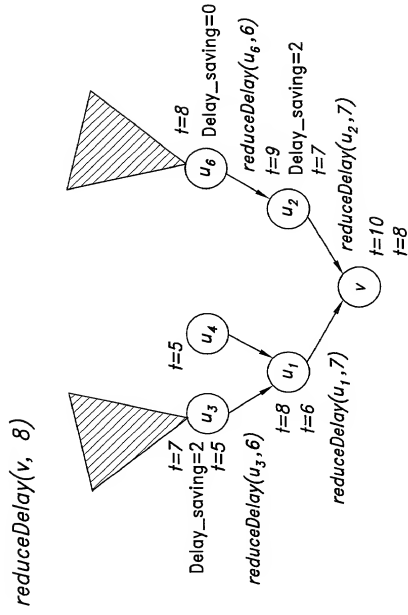
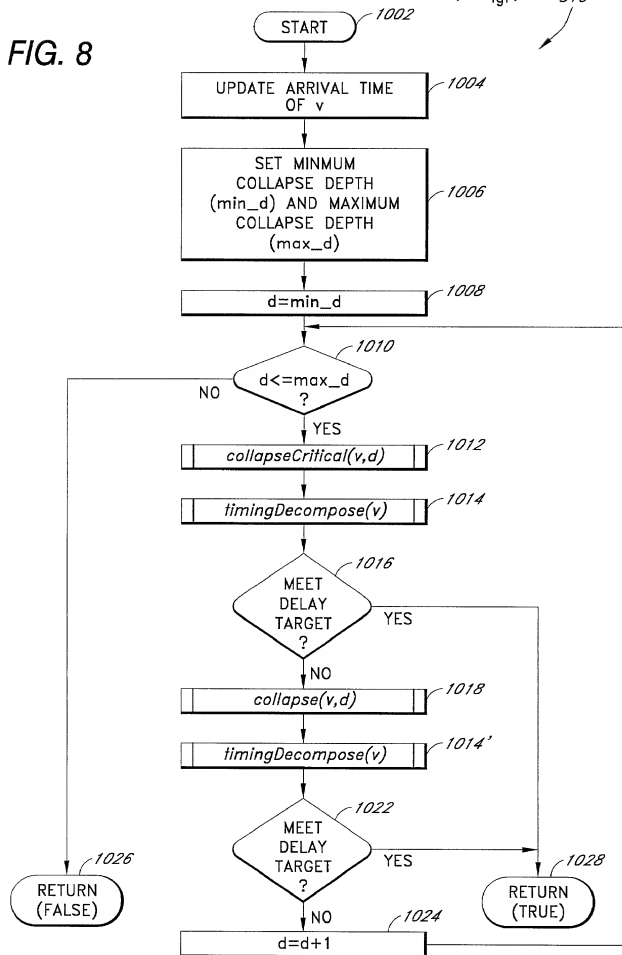


FIG. 7

LOCAL TRANSFORMATION - transform(v, d_{tgt})

FIG. 8



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Impact of Timing Optimization

the impact of timing optimization on the subsequent mapping, clustering and layout design

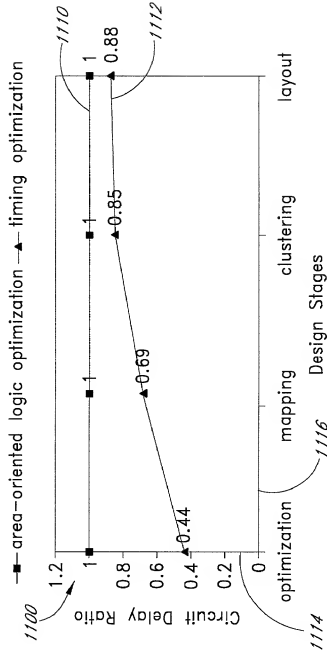


FIG. 9

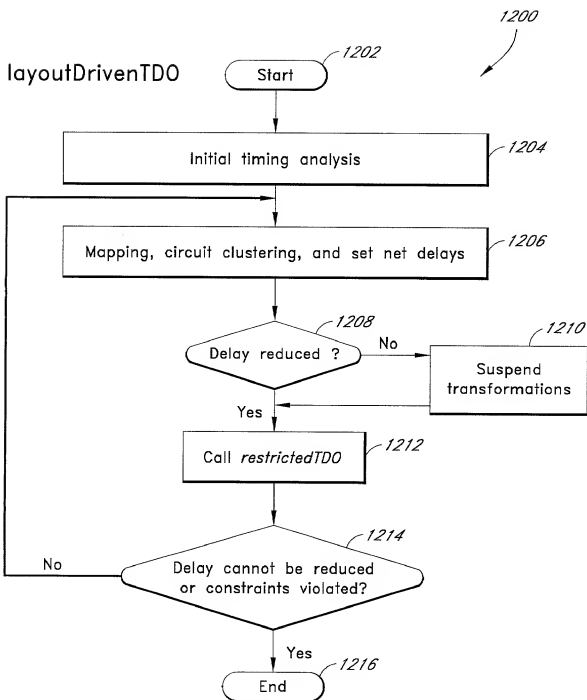


FIG. 10